## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U	J.S. Patent Application of	)
MITS	UISHI	)
Applic	cation Number: To be Assigned	)
Filed:	Concurrently Herewith	.)
For:	DATA TRANSFER DEVICE, SEMICONDUCTOR INTEGRATED CIRCUIT, AND MICROCOMPUTER	)
ATTOI	ONEV DOCKET NO. HITA 0453	)

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

## REQUEST FOR PRIORITY UNDER 35 U.S.C. § 119 AND THE INTERNATIONAL CONVENTION

Sir:

In the matter of the above-captioned application for a United States patent, notice is hereby given that the Applicant claims the priority date of January 20, 2003, the filing date of the corresponding Japanese patent application 2003-010739.

A certified copy of Japanese patent application 2003-010739, will be submitted as soon as it is received from Applicant. Acknowledgment of receipt of the certified copy is respectfully requested in due course.

Respectfully submitted,

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REED SMITH LLP

3110 Fairview Park Drive Suite 1400 Falls Church, Virginia 22042 (703) 641-4200 November 13, 2003